

0022610-216829960

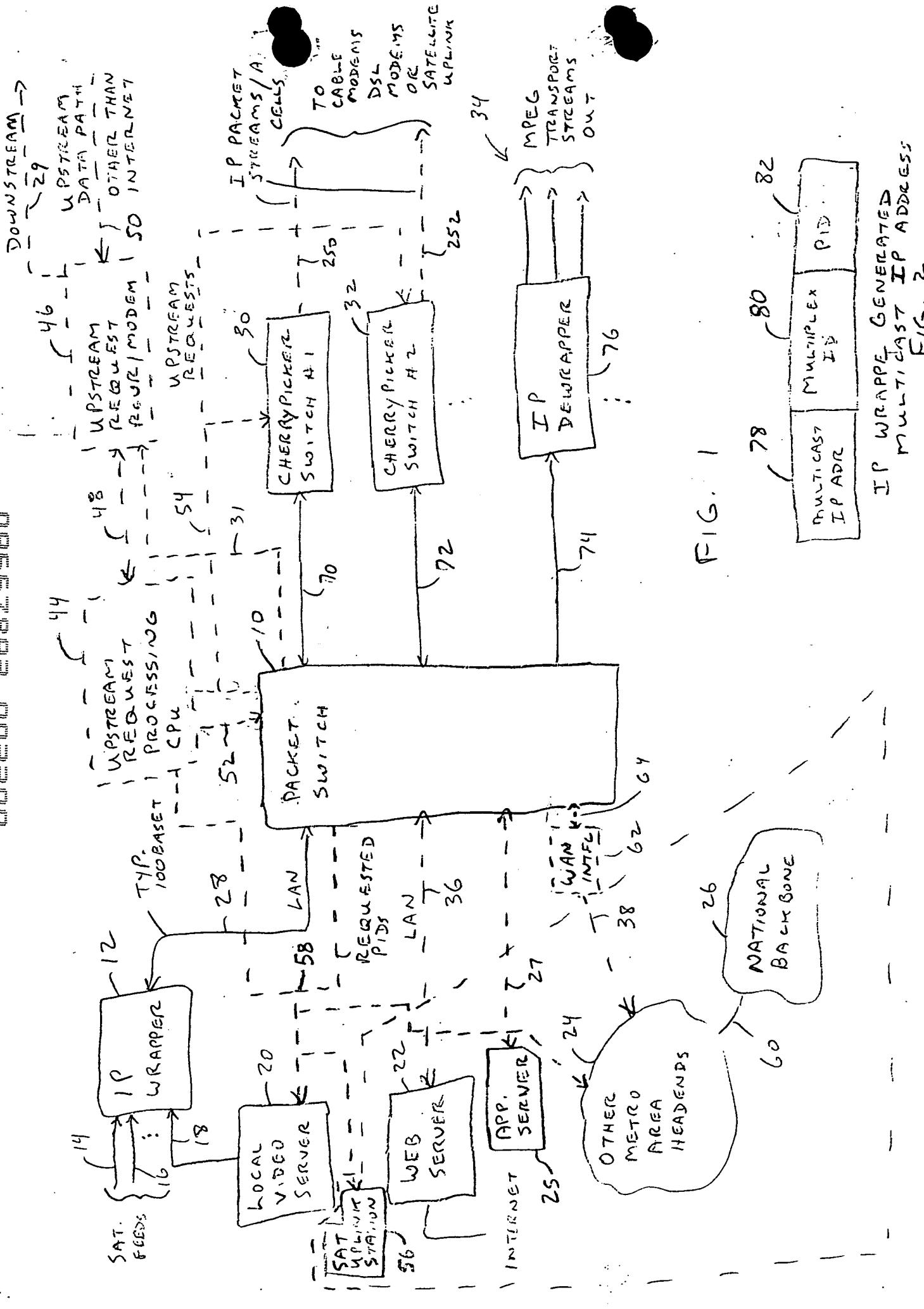
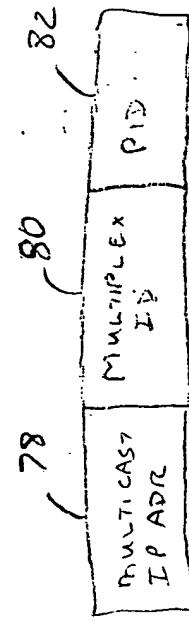


FIG. 1



IP WRAPPER GENERATED MULTICAST IP ADDRESS
FIG. 2

PROCESS OF ENCAPSULATION CARRIED OUT IN IP WRAPPER FPGA

0022601-26829950

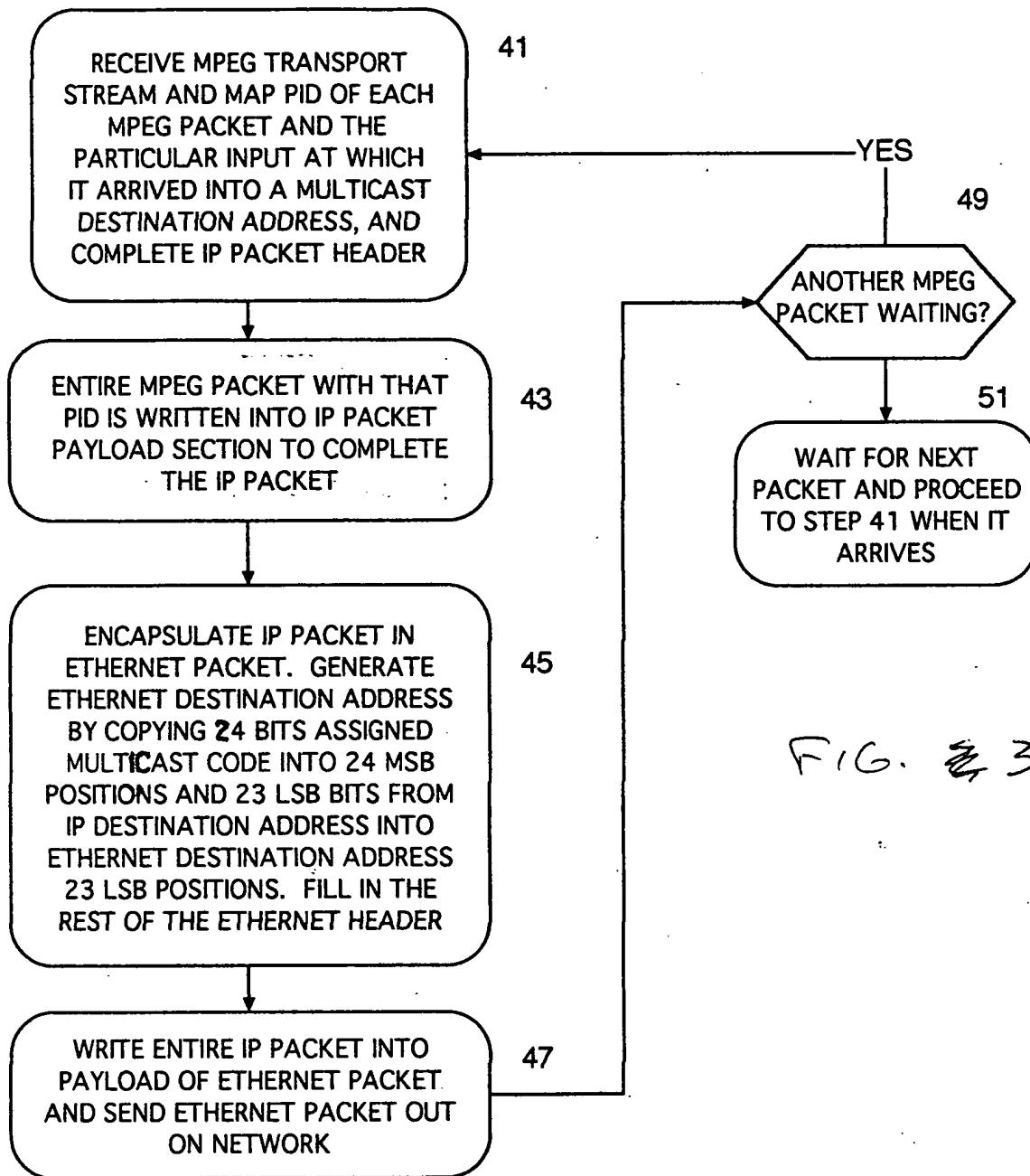


FIG. 3

PREFERRED ENCAPSULATION PROCESS CARRIED OUT BY IP WRAPPER

0002200 - 0002200 - 0002200 - 0002200 - 0002200

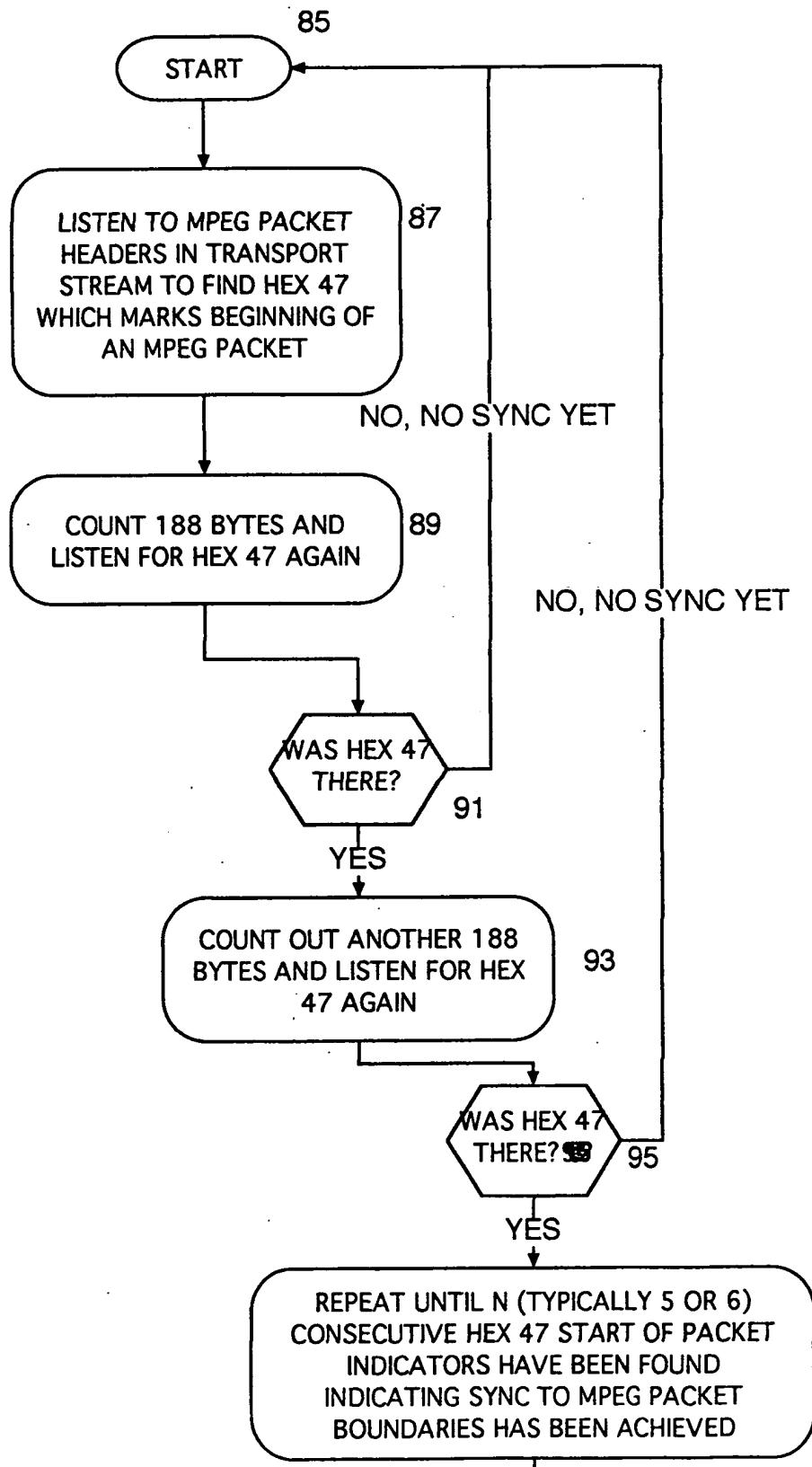


FIG. 4A

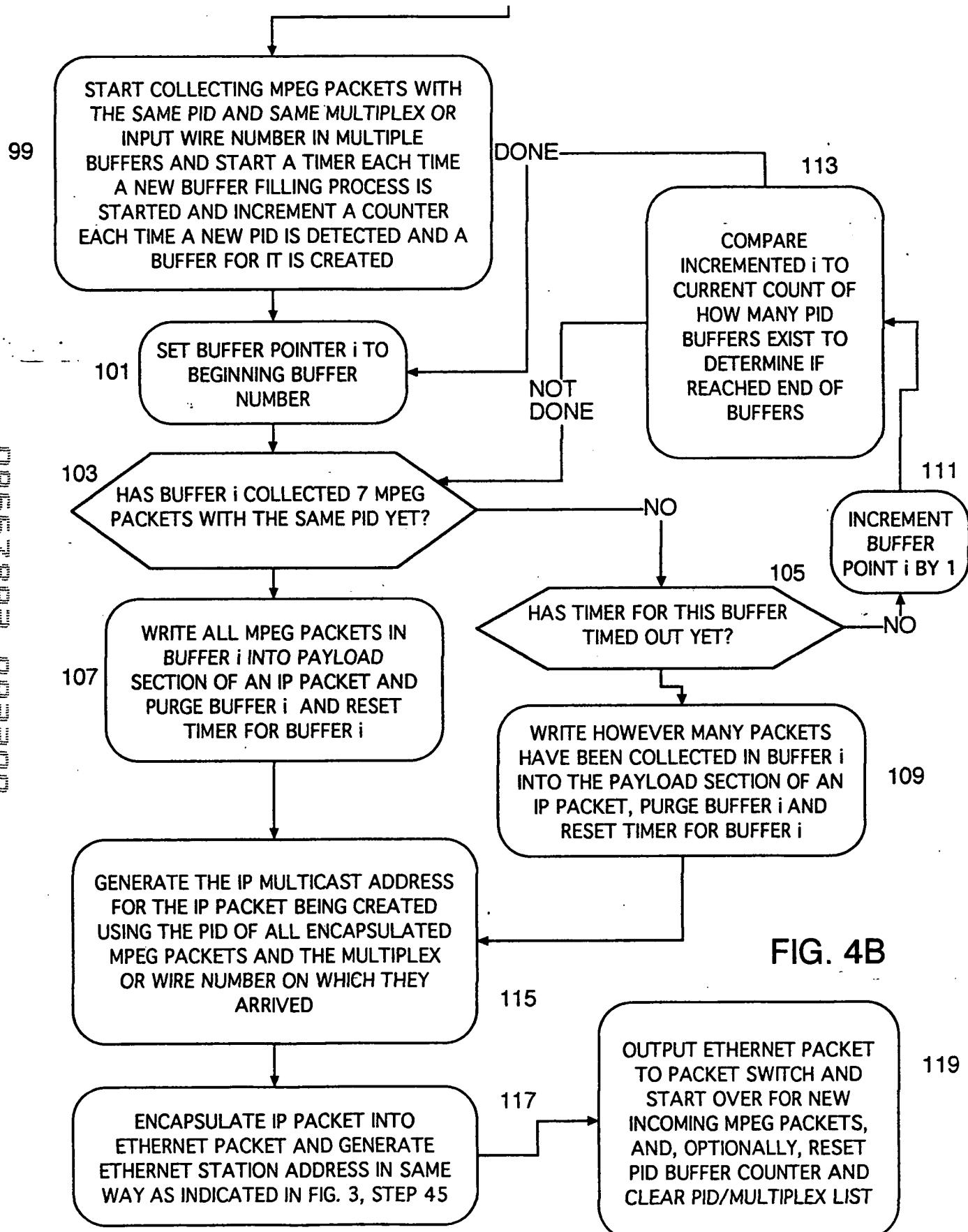


FIG. 4B

PROCESS OF GENERATING MPEG TRANSPORT STREAMS IN CHERYPICKER SWITCHES

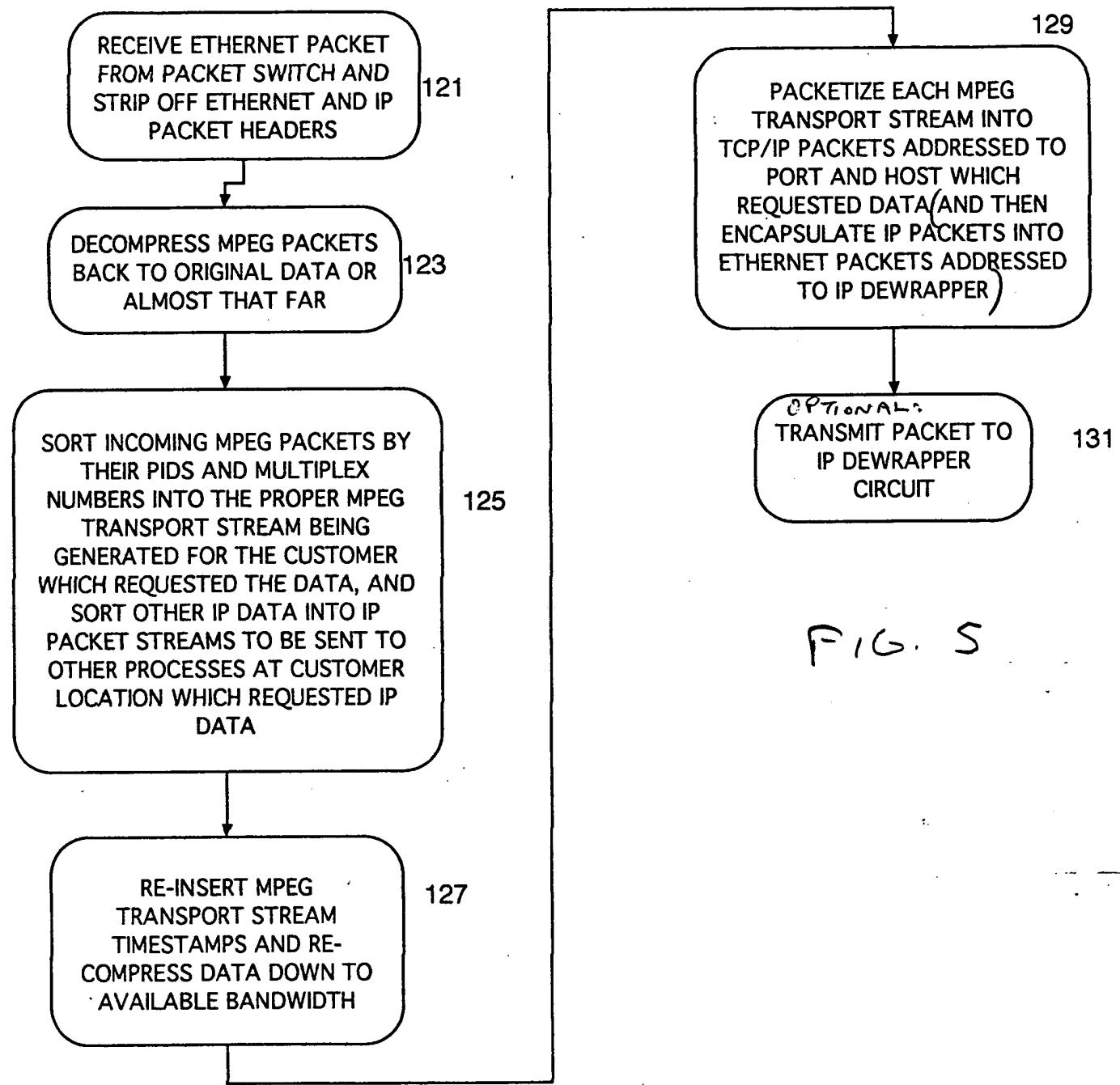
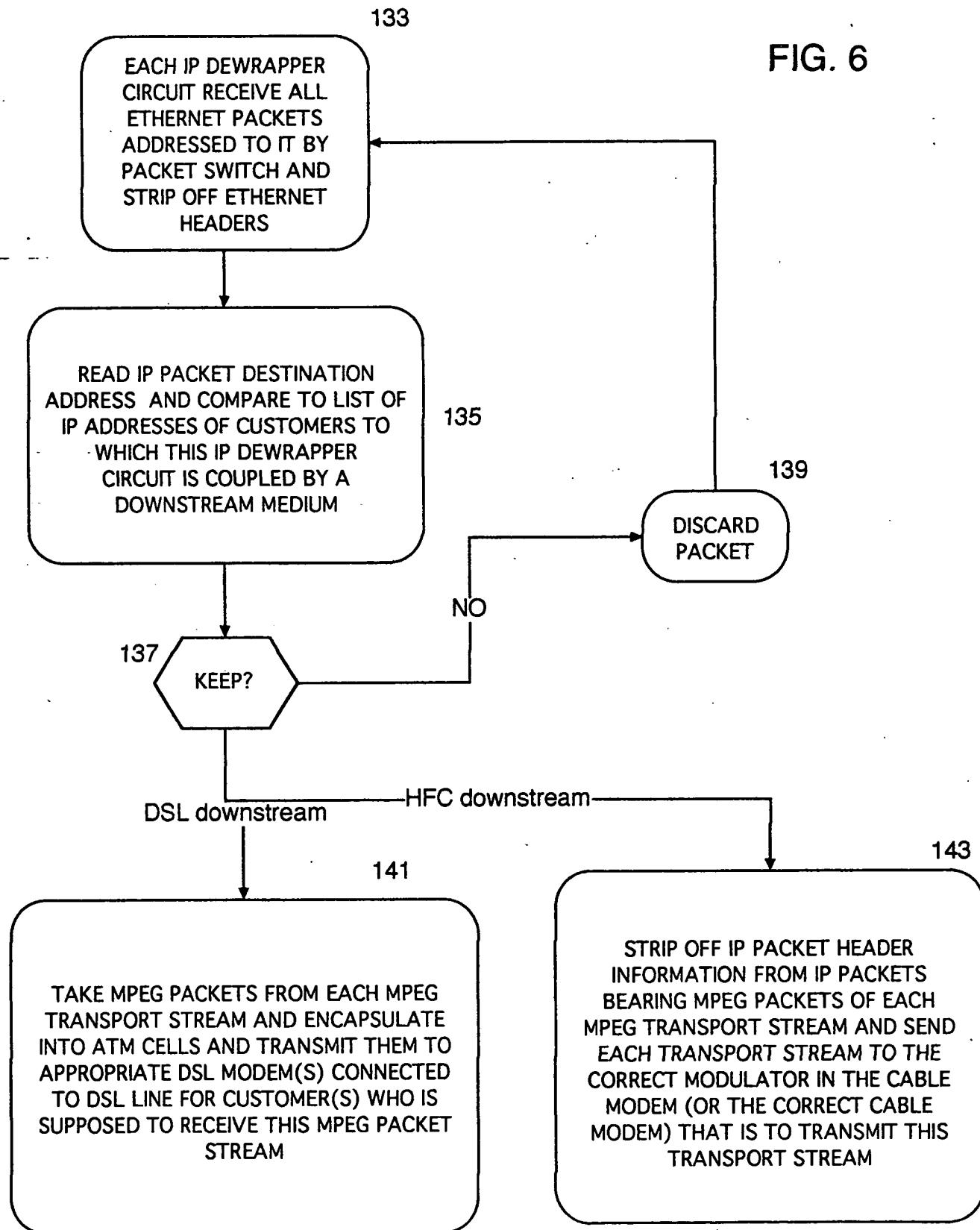
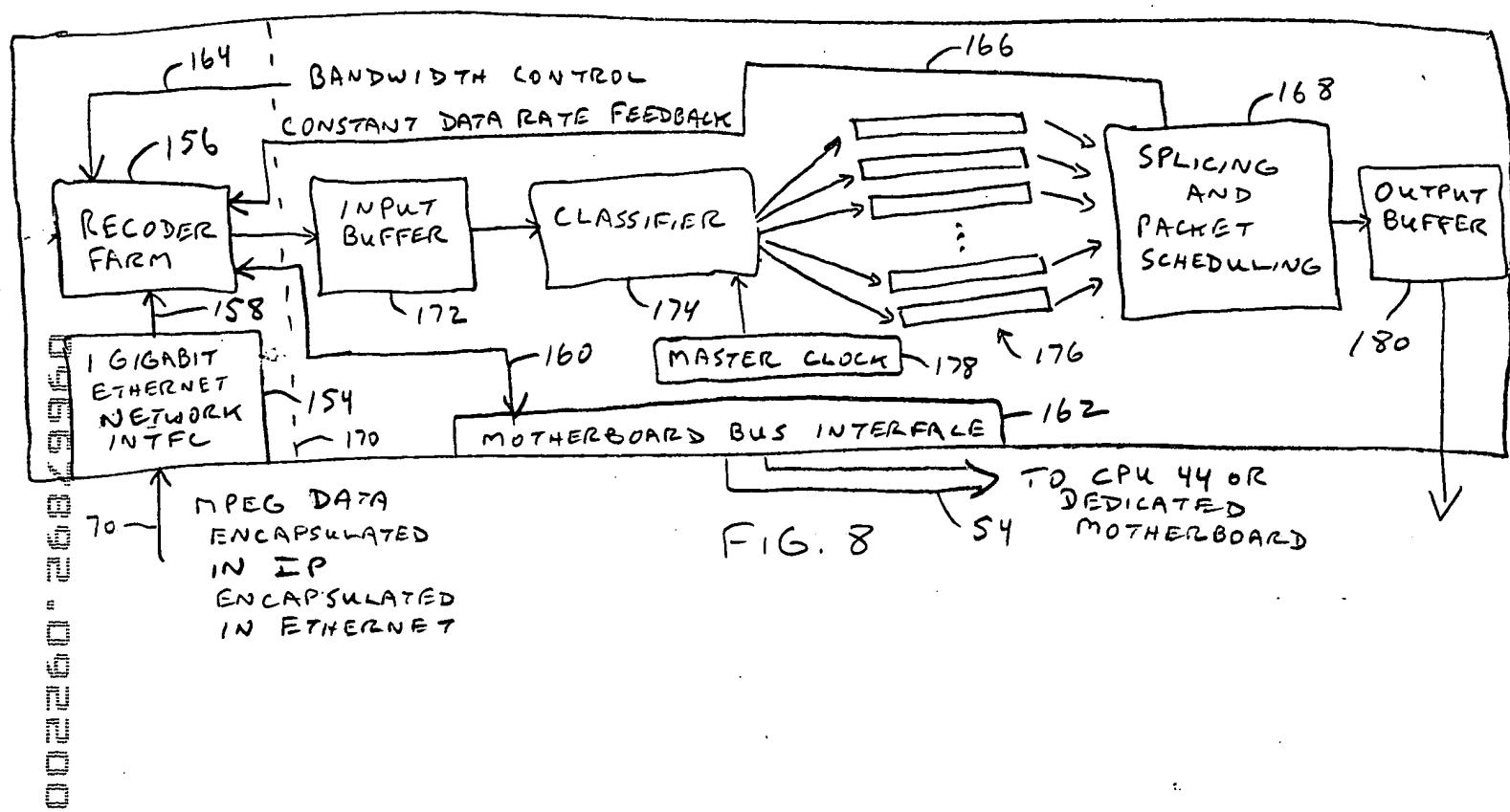
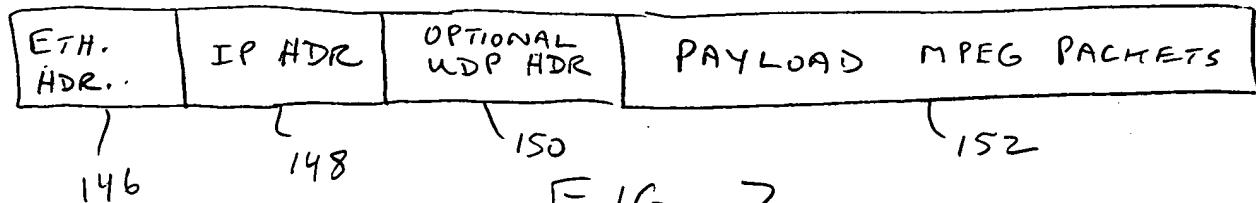


FIG. 5

IP DEWRAPPER PROCESS FOR DSL OR HFC ENVIRONMENTS





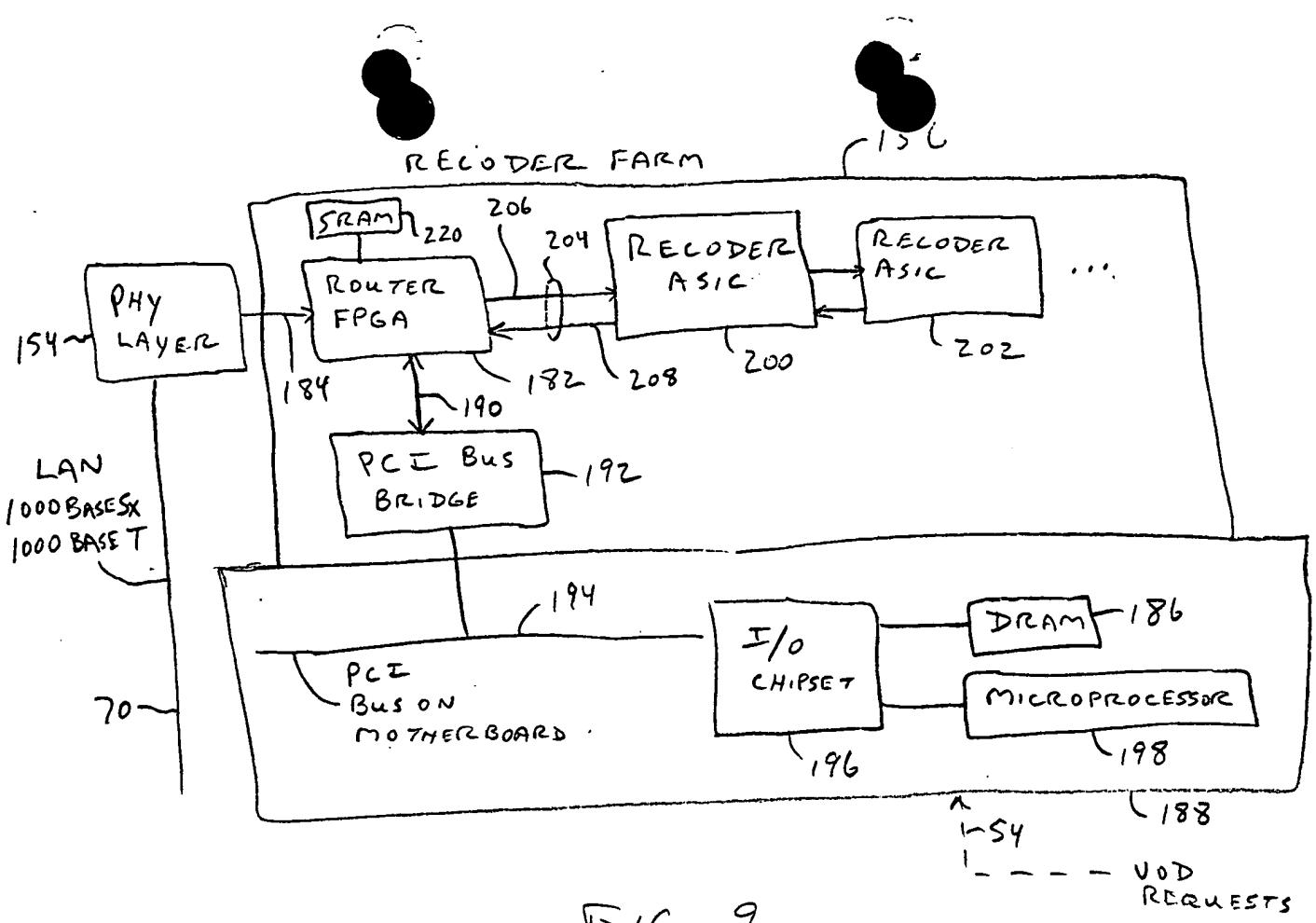


FIG. 9

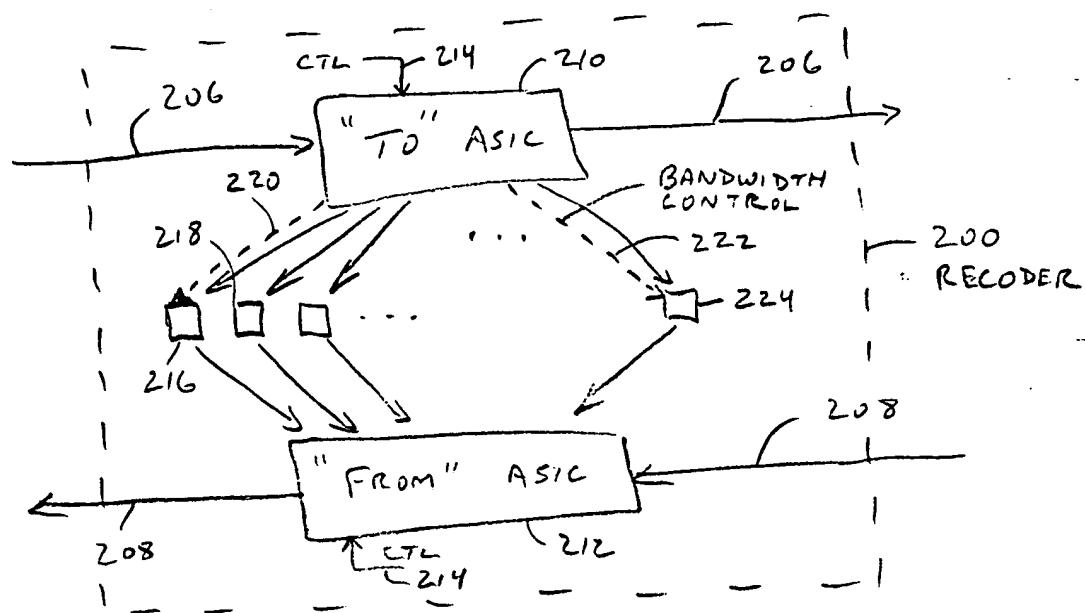


FIG. 10

002260-26829960

HEADEND ARCHITECTURE TO SEND DATA AND
VOD AND/OR BROADCAST DATA TO CUSTOMERS
VIA HFC

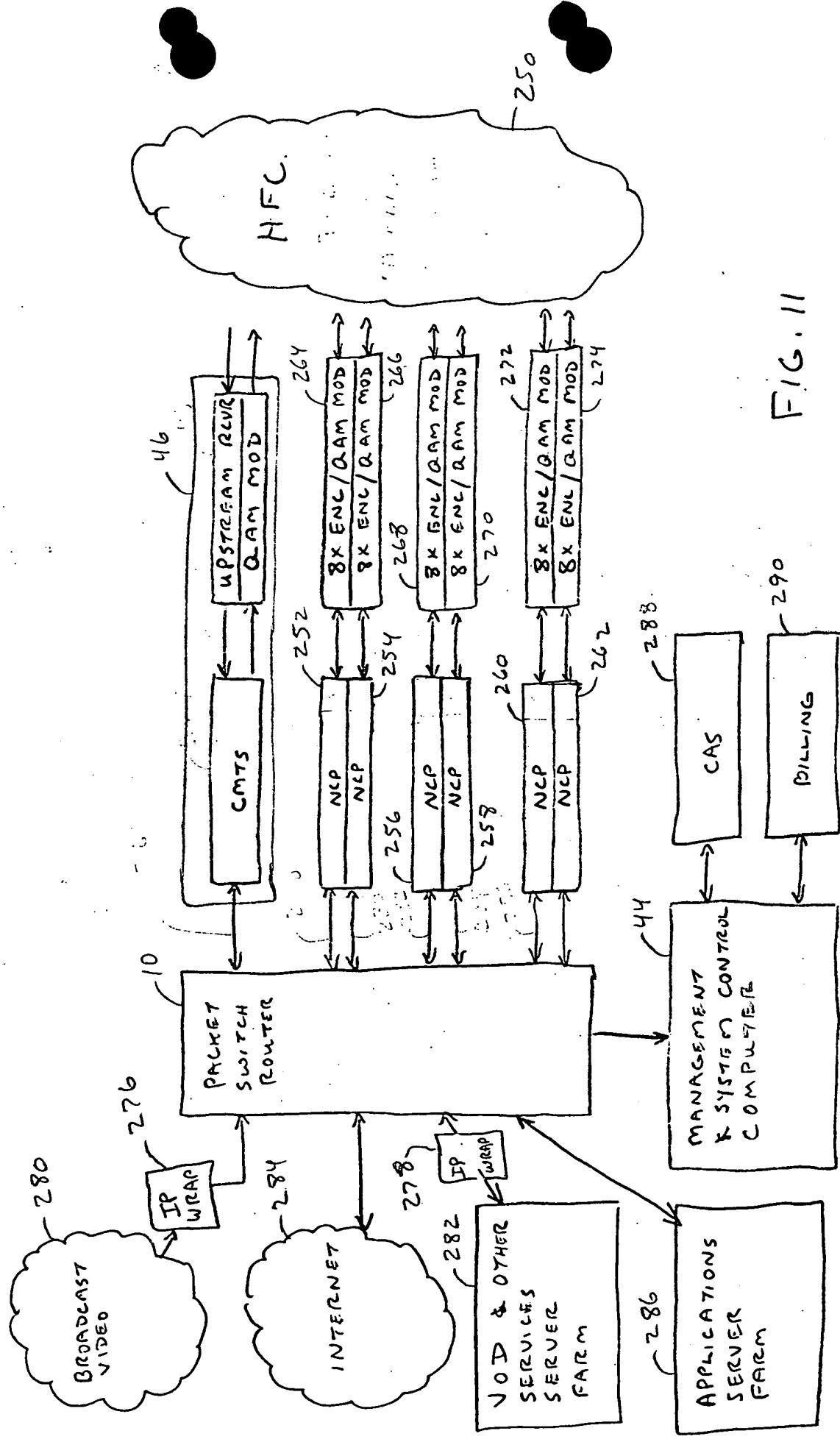


FIG. 11

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EMBODIMENT FOR TRANSMISSION OF DATA AND VIDEO
PROGRAM DATA FROM HEADEND TO CUSTOMERS

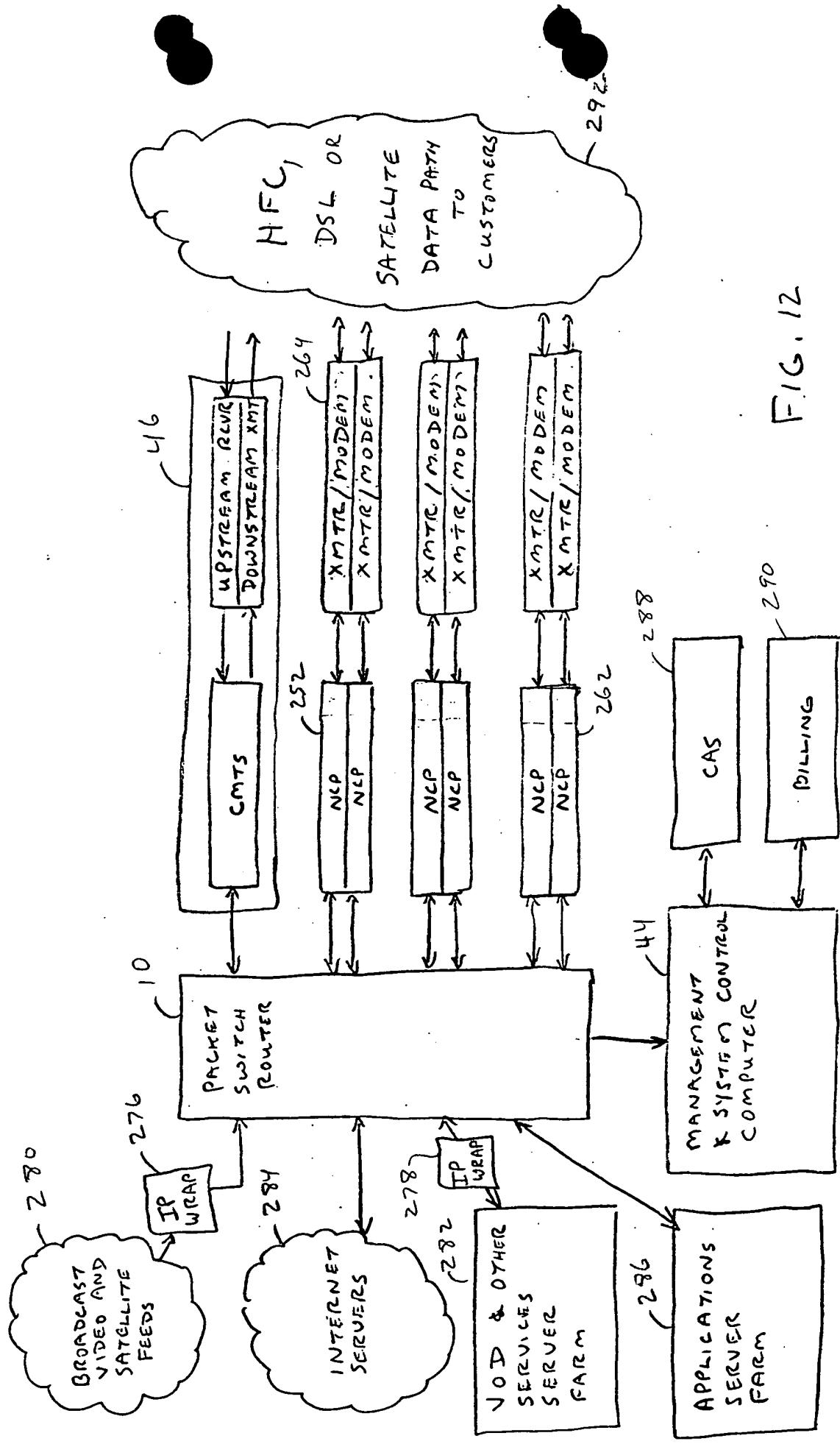


FIG. 12